

# Publications

- Samoda Gamage and Ajith Pasqual, “High performance parallel packet classification architecture with popular rule caching,” *18th IEEE International Conference on Networks (ICON)*, Singapore, Dec. 2012.
- Samoda Gamage and Ajith Pasqual, “Line rate packet classification module for NetFPGA,” *8th IEEE International Conference on Industrial and Information Systems (ICIIS)*, Sri Lanka, Dec. 2013.



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